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	Dear	25 X 1
	The attached specification incorporates the revisions as agreed	
	upon at the joint conference held at January 23, 1959.	25X1
	The nature of this document prevents our forwarding a copy to	
	but a copy for is attached and it is	25 X 1
	requested that you forward his copy in accordance with your secu- rity procedure.	25 X 1
	Very truly yours,	0EV4
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	Enclosures Set of Specifications).).
	Copy of Specifications for	25X1

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SECRET

Signal:

- 1. ALERT LINE. The Alert line will provide the Data Collector with a negative-going lh volt step (0 to -lh volt unregulated, @ 15 milli-amperes) for the purpose of initiating the Data Collector Turn-On function. The -lh volt DC level shift will occur approximately 3 to 5 seconds prior to the first clock pulse of each readout operation and terminate at the end of the transmit sequence. During the time when the -lh volt signal is not present, the Alert line will be grounded.
- 2. CLOCK or READOUT LINE. The Readout line will provide the Data Collector with clock pulses having the following specifications: Amplitude, 5 volts (0 to +5) minimum; duration, 175 ± 25 microseconds; repetition rate, 200 pps. The Digital Converter will provide these clock pulses continuously during the time interval which commences approximately 3 to 5 seconds following the Alert signal, and terminates upon receipt of a stop pulse from the Collector.
- Converter with the information stored in the Collector data memory unit. The first bit of information will be emitted by the Collector upon receipt of the first readout pulse from the Digital Converter and the time delay therein will be a maximum of 10 microseconds. The above measurement is to be made between the leading edge of the readout pulse and the leading edge of the information pulse at the half-power points. The information pulses will have the following parameters: Amplitude, 5 volts (0 to +5) minimum; duration, 100 * 25 microseconds; rise time, 8 microseconds maximum. The Collector will gate the Information line off until such time that the Collector is reset and ready to supply data. It is essential that the information line is free of any spurious spikes, etc. which might accidentally trigger the message circuitry in the Digital Converter.

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- STOP PULSE LINE. The Stop Pulse line will provide the Digital Converter with a single pulse which is time coincident with the last information bit received from the Collector. This pulse will consist of a differentiated spike with the following parameters: Amplitude, 5 volts (0 to +5) minimum; rise time, 2 microseconds maximum; decay time, 3 milliseconds (approximately). It is essential that this line be free of spurious spikes, etc.
- CLEAR LINE. The Glear line will provide the Collector with a clearing signal consisting of a negative-going step (-14 volt, unregulated) which will be capable of delivering at least 250 milliamperes of current. An additional line will be provided in order to facilitate a ground return for the clearing signal.
- 6. PANIC LINE. The Data Collector will provide a Panic pulse (level shift) which will consist of a negative 10 volt step (0 to -10 volt) capable of supplying 15 milliamperes of current. In order to facilitate the above, a relay and a turn-off driver circuit will be added to the Collector package. As Panic occurs, the latching relay is energized thus applying the Panic signal to the Panic line. The Panic signal will be held on until the next Alert signal is received. At that time, the turn-off driver circuit releases the latching relay and thereby removes the signal on the Panic line. If the Panic condition still exists after the Alert signal has released the latching relay, the above sequence will be repeated until such time that the panic condition is no longer in affect. Power:
- The inter-unit connections will be made through a fourteen-conductor cable (supplied with the collector package) which will be approximately six feet in length. The connector will be at the Data Converter end of the cable. R.F. filtering for the cable will be contained in the collector package. numbers of the connector are assigned as follows: meming of the Esplanego Laws, Title 12, U.S. 24, Section 25X1 Distrission or Revolution of which in any memory is an

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Secret Document No. RD9-1108/23
Page 3 of 3 Copy No. 1066-25X1

Pin No:

- A. Alert Line
- B. Readout (Clock) Line
- C. Information Line
- D. Stop Pulse Line
- E. Clear Line
- F. Panic Line
- H. +14 volts
- J. + 7 volts
- K. 7 volts
- L. -- ll volts
- M. System Ground
- N. Ground Return for Clear Line
- P. -----
- R. ----

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